

FACULTY PROFILE						
1.	Name	Rakhi S				
2.	Date of Birth (dd/mm/yyyy)	29/12/1977				
3.	UID (Aadhar No.)	xxxxxxxx24184				
4.	e-Mail ID (Official)	rakhi.s@atria.edu				
5.	Designation	Assistant Professor				
6.	Department	ECE				
7.	Google Scholar Link:	Rakhi S				
8.	Area of Specialization	VLSI Design				
9.	Courses Taught (UG)	Basic Electronics, Digital Electronics, Digital System, HDL Programming, CMOS VLSI Design, CCN, Network Analysis, Wireless Communication, GSM, ITC, Cellular Mobile Communication, Logic Design, Optical Communication, Programming in C++, Electronic Instrumentation, Power Electronics, Control System				
10.	Courses Taught (PG)	Cryptography				
11.	QUALIFICATION DETAILS:					
12.	Total Experience in Years	Teaching	Industry	Research		
		16	2	5(PhD)		
13.	Publication Details	<i>Type/Level</i>	National		International	
		Journal	--		4	
		Conference	1		2	
14.	No. of Research and Projects Guided	12(UG)				
15.	No. of Books Published/IPRs/Patents	--				
16.	Sponsored Projects/Grants (if any)	KSCST SSP-2020				
17.	Professional Memberships	ISTE				
18.	Coordinator/In-Charge/Part, of NAAC/NBA/any other Accreditations.	VTU eshikshana Coordinator, LMS Coordinator, Lab In charge, NAAC-Criteria 1,Class Coordinator, IETE Coordinator, NBA-Criteria 1				
19.	Academic Achievements	M Tech First Rank with Gold Medal				
20.	Extra-Curricular Achievements	--				
21.	Co-Curricular Achievements	--				
22.	No. of Workshops/Seminars Conducted	5				
23.	No. of Workshops/Seminars Attended	25				
24.	Sports Achievements	--				
25.	Awards and Recognition (If any)	DST AWSAR Award 2019				