FACULTY PROFILE

1.	Name	S.Kavitha
2.	Date of Birth (dd/mm/yyyy)	29/05/1975
3.	UID (Aadhar No.)	#### #### 5975
4.	e-Mail ID (Official)	kavitha.s@atria.edu
5.	Designation	Assistant Professor
6.	Department	Electronics and Communication Engineering
7.	Google Scholar Link:	uC96I04AAAAJ
8.	Area of Specialization	VLSI design and Embedded System
9.	Courses Taught (UG)	Basic Electronics,Verilog HDL,Network Analysis,Microprocessor,,Programming in C++,Digital switching system,Digital system design using Verilog,CMOS VLSI design,Power Electronics and Instrumentation,Embedded system design,8051 Microcontroller ,ARM Microcontroller and Embedded system ,HDL Programming ,Programming in C
10.	Courses Taught (PG)	NIL
11.	QUALIFICATION DETAILS:	B.E-Electronics and Communication Engineering M.Tech-VLSI Design and Embedded system

		Teaching	Industry	Research
12.	Total Experience in Years	17.4	NIL	NIL

		Type/Level	National	International
13.	Publication Details	Journal	2	-
		Conference	1	-

14.	No. of Research and Projects Guided	13Batches
15.	No. of Books Published/IPRs/Patents	NIL
16.	Sponsored Projects/Grants (if any)	NIL
17.	Professional Memberships	NIL
18.	Coordinator/In-Charge/Part, of NAAC/NBA/any other Accreditations.	Class co ordinator,Lab-in charge,NAAC Criteria-5,NBA Criteria-2,Dept NIRF coordinator,VTU exam internal DCS and dept coordinator.
19.	Academic Achievements	• Received merit scholarshipfor higher studies based on SSLC marks • Done 3 MOOC courses
20.	Extra-Curricular Achievements	
21.	Co-Curricular Achievements	
22.	No. of Workshops/Seminars Conducted	Resource person in 2 SDPs
23.	No. of Workshops/Seminars Attended	15
24.	Sports Achievements	-
25.	Awards and Recognition (If any)	Received "Elite with silver" in NPTEL course