

## Atria Institute of Technology, Bengaluru – 560 024

		FAC	CULTY PROFILE			
1.	Name		Poornima.B			
2.	Date of Birth(dd/mm/yyyy)		23/12/1969			
3.	UID (Aadhar No.)		**** **** 9163			
4.	e-Mail ID (Official)		Poornima.b@atria.edu			
5.	Designation		Asst. Professor			
6.	Department		Electronics and Communication Engg			
7.	Google Scholar Link:		Water and British			
8.	Area of Specialization		VLSI Design & Embedded Systems			
9.	Courses Taught (UG)		<ul> <li>VLSI Design</li> <li>Controls</li> <li>Microcontrollers</li> <li>CMOS VLSI</li> <li>Digital Electronics</li> <li>Basic Electricals</li> <li>Computer Organisation &amp; Architecture</li> </ul>			
10.	Courses Taught (PG)					
11.	QUALIFICATION DETAILS:		Diploma in Computer Science  B.E in Electricals & Electronics, Mysore University  M.Tech in VLSI Design and Embedded systems, VTU			
12.	Total Experience in Years		Teaching	Indust	ry	Research
12.			22.5	01		
	Publication Details	Type/Level	Nation	al		International
13.		Journal				09
		Conference				01
14.	No. of Research and P	rojects Guided				
15.	No. of Books Published/IPRs/Patents		02			
16.	Sponsored Projects/Grants (if any)					
17.	Professional Members					
18.	Coordinator/In-Charge/Part, of NAAC/NBA/any other Accreditations.		<ul> <li>Chief co-ordinator of VIDYAPATHA - (Education Fair )</li> <li>Chief time-table co-ordinator</li> <li>Co-ordinator for one day workshop &amp; seminar</li> <li>Project co-ordinator for 5 plus yrs.</li> <li>Seminar co-ordinator for 5 plus yrs.</li> <li>Time-table co-ordinator for 5 plus yrs.</li> <li>Part of NBA - Criteria 2</li> <li>Part of NAAC - Criteria 7</li> </ul>			
19.	Academic Achievements		Atria Ins	<ul> <li>Headed the Dept. of Electronics &amp; Communication at         Atria Institute of Technology, Bangalore     </li> <li>In-charge of EDC Cell (Educational &amp; Development cell ).</li> </ul>		

		Convener for one day workshop & seminar				
		Reviewer for Flip –class(Integrated methodology of				
		coaching ) - Xcelerator				
		Member of Anti –ragging Committee for 7 plus yrs.				
		Member of Flying –Squad Committee.				
		Worked as DCS for VTU exams.				
		Instrumental in Initiation and setting up of the following				
		labs :				
		1. VLSI lab 2. Microcontrollers lab,				
		3. LD lab, 4. Power Electronics lab				
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		Member of Cultural Committee for 5 plus yrs - Atria Litsay				
20.	Extra-Curricular Achievements	Member of Cultural Committee for 5 plus yrs - Atria Utsav, Saptharang				
		Mentor –Regular students				
21.	Co-Curricular Achievements	Mentor – AICTE student activity				
22.	No. of Workshops/Seminars Conducted	0.5				
23.	No. of Workshops/Seminars Attended	15				
24.	Sports Achievements					
25.	Awards and Recognition (If any)					