



ATRIA INSTITUTE OF TECHNOLOGY
 ANSDC Campus, 1st Main Rd., 5th Cross, Anandnagar, Hobli, Bengaluru, Karnataka 560011, India
 Department of Electronics and Communication Engineering
CLASS TIME-TABLE FOR THE ACADEMIC YEAR 2023 - 24 (3RD Semester) (From SEP to DEC 2023)
 Class Coordinator : Dr.Sathyasrikanth Palle

Semester : 7th	Section : A		Classroom No (C.C. 11)						
Periods	1	2	3	4	5	6	7		
Time									
Day	9:00 - 10:00 AM	10:00 - 11:00 AM	11:00 - 11:15 AM	11:15 - 12:15 PM	12:15 - 1:15 PM	1:15 - 2:00 PM	2:00 - 3:00 PM	3:00 - 4:00 PM	4:00 - 5:00 PM
MON	CN	VLSI	Tea Break	CN	VLSI	Lunch Break	CN-A1(8S, RN)/VLSI-A2(8S, RP)		
TUE	RTSSATCOM/	Multimedia/IOT		EE/PM/AI	CN		PROJECT WORK		
WED	VLSI	RTSSATCOM/		EE/PM/AI	Multimedia/IOT		CN-A2(8S, RN)/VLSI-A1(8S, RP)		
THU	Multimedia/IOT	RTSSATCOM/		EE/PM/AI			INTERNSHIP		
FRI	CN	VLSI		RTSSATCOM	Multimedia/IOT				
SAT	VLSI	CN		MENTOR-MENTEE					

Subject	Subject Name	Faculty
18EC71	Computer Networks	Dr.Sathyasrikanth Palle
18EC72	VLSI Design	Dr.Rakhi S
18EC73/18732	Real Time System/Satellite Communication	Prof.Kavitha S/Jayanth C/Prof.Shotha PC/Prof.Pushpa Y
18EC74/743	IOT & Wireless Sensor Networks/Multimedia Communication	Prof.Prasanna V/Prof.Gayathri J
18ECL76	Computer Networks lab	Dr.Sathyasrikanth Palle
18ECL77	Vlsi Design lab	Dr.Rakhi S
18ECP78	Project Work Phase I	Dr.Keshvamurthy
18EC1	Internship	Dr. Sathyasrikanth
18ME751/18A753/18CS753	Energy and Environment/Environmental protection	Dept. of Mech/Civil/CS

LAB 1 NAME: Computer Networks Lab(18ECL76)	LAB 2 NAME: VLSI Lab(18ECL77)
LAB 1 IN CHARGE: Dr. Sathyasrikanth Palle	LAB 2 IN CHARGE: Dr. Rakhi S
CN lab-C1	VLSI lab-C1
Dr. Sathyasrikanth Palle	Dr. Rakhi S
Dr. Ramesh N	Prof. Suresh S P
CN lab-C2	VLSI lab-C2
Dr. Sathyasrikanth Palle	Dr. Rakhi S
Dr. Ramesh N	Prof. Suresh S P

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PROF. & HEAD OF THE DEPARTMENT
 DEPARTMENT OF ELECTRONICS & COMMUNICATION
 ATRIA INSTITUTE OF TECHNOLOGY
 ANANDNAGAR, BENGALURU - 560 024

PRINCIPAL
 Principal
 Atria Institute of Technology
 Anandnagar, Bengaluru-24



ATRIA INSTITUTE OF TECHNOLOGY
 ASKB Campus, 1st Main Rd, Aps Colony, Anandnagar, Hebbal, Bengaluru, Karnataka 560024, India
 Department of Electronics and Communication Engineering
 CLASS TIME-TABLE FOR THE ACADEMIC YEAR 2023-24 (ODD Semester) - From SEP to DEC 2023
 Class Coordinator - Prof. N

Classroom No: CC 314

Semester: 7th		Section: C				Classroom No: CC 314				
Periods	1	2	3	4	5	6	7			
Time										
Day	9:00 - 10:00 AM	10:00 - 11:00 AM	11:00 - 11:15 AM	11:15 - 12:15 PM	12:15 - 1:15 PM	1:15 - 2:00 PM	2:00 - 3:00 PM	3:00 - 4:00 PM	4:00 - 5:00 PM	
MON	CN	CN-C1(RN,SS)/	Tea Break	VLSI-C2(X,PV)		Lunch Break	VLSI	CN		
TUE	RTS/SATCOM/	Multimedia/IOT		EE/EPMAI	CN		PROJECT WORK			
WED	VLSI	RTS/SATCOM/		EE/EPMAI	Multimedia/IOT		INTERNSHIP			
THU	Multimedia/IOT	RTS/SATCOM/		EE/EPMAI	VLSI					
FRI	CN	VLSI		RTS/SATCOM/	Multimedia/IOT		CN-C2(RN,SS)/VLSI-C1(X, PV)			
SAT	Cluster Activity									

Subject	Subject Name	Faculty
ISEE 71	Computer Networks	Dr. Ramesh N
ISEE 72	VLSI Design	Prof. N
ISEE 73/18732	Real Time System/Satellite Communication	Prof. Kavitha S/Gayathri U/Prof. Shobha PC/Prof. Pushpa Y
ISEE 74/743	IOT & Wireless Sensor Networks/Multimedia Communication	Prof. Prasanna V/Prof. Gayathri J
ISEE 76	Computer Networks lab	Dr. Ramesh N
ISEE 77	VLSI Design lab	Prof. N
ISEE P78	Project Work Phase I	Dr. Keshvamurthy
ISEE 7	Internship	Dr. Sathya Srikanth Palle
ISEE 79/1	Environmental protection and management	
ISEE 78/4	Introduction to Artificial Intelligence	Dept. of Mech/IEES
ISEE 78/4		

LAB 1 NAME: Computer Networks Lab (ISEE 76)		LAB 2 NAME: VLSI Lab (ISEE 77)	
LAB 1 INCHARGE: Prof. Anappa C		LAB 2 INCHARGE: Prof. N	
CN lab-C1	Dr. Ramesh N	VLSI lab C2	Prof. N
	Dr. Sathya Srikanth Palle		Dr. Prasanna V
CN lab-C2	Dr. Ramesh N	VLSI lab C1	Prof. N
	Dr. Sathya Srikanth Palle		Dr. Prasanna V

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PROF. & HEAD OF THE DEPARTMENT
 DEPARTMENT OF ELECTRONICS & COMMUNICATION
 ATRIA INSTITUTE OF TECHNOLOGY
 ANANDNAGAR, BENGALURU - 560 024

Malini
 PRINCIPAL 25/9/2023
 Principal
 Atria Institute of Technology
 Anandnagar, Bengaluru-24



ATRIA INSTITUTE OF TECHNOLOGY
 ASKB Campus, 1st Main Rd, Ags Colony, Anandnagar, Hebbal, Bengaluru, Karnataka 560024, India
 Department of Electronics and Communication Engineering
CLASS TIME TABLE FOR THE ACADEMIC YEAR 2023 - 24 (O/EO Semester) | 3 from SEP to DEC 2023
 Class Coordinator: Prof. Spourthi SP

Semester : 7th		Section : B		Classroom No:CC-313							
Periods	1	2		3	4		5	6	7		
Time											
Day	9.00 - 10.00 AM	10.00 - 11.00 AM	11.00 - 11.15 AM	11.15 - 12.15 PM	12.15 - 1.15 PM	1.15 - 2.00 PM	2.00 - 3.00 PM	3.00 - 4.00 PM	4.00 - 5.00 PM		
MON	CN	VLSI	Tea Break	CN	Mentor-Mentee	Lunch Break	CN-B1(AC.G.)VLSI-B2(SE,RS)				
TUE	RTSSATCOM/	Multimedia/IOT		EE/EPMAI	CN		PROJECT WORK				
WED	VLSI	RTSSATCOM		EE/EPMAI	Multimedia/IOT		CN-B2(AC.G.)VLSI-B1(SP,RS)				
THU	Multimedia/IOT	RTSSATCOM		EE/EPMAI	VLSI		INTERNSHIP				
FRI	VLSI	CN		RTSSATCOM/	Multimedia/IOT						
SAT	Cluster Activity										

Subject	Subject Name	Faculty
DEC 71	Computer Networks	Prof. Annappa C
DEC 72	VLSI Design	Prof. Spourthi
DEC 73/DEC 72	Real Time System-Satellite Communication	Prof. Kavitha S./Jyothi U/Prof. Sneha PC/Prof. Parshya Y
DEC 74/743	IOT & Wireless Sensor Networks/Multimedia Communication	Prof. Prayana V./Prof. Gayathri J
IBEC176	Computer Networks lab	Prof. Annappa C
IBEC177	VLSI Design lab	Prof. Spourthi SP
IBEC178	Project Work Phase I	Dr. Keshvanurthy
IBEC1	Internship	Dr. Sarbajit Ghosh
DSM 751, 86, A 752, 18CS753	Energy and Environment/Environmental protection	Dept. of Mech/CO-3

LAB 1 NAME: Computer Networks Lab(IECE 76)		LAB 2 NAME: VLSI Lab(IECE 77)	
LAB 1 INCHARGE: Dr. Himesh N		LAB 2 INCHARGE: Prof. Spourthi S P	
CN lab-C1	Prof. Annappa C	VLSI lab C2	Prof. Spourthi S P
	Prof. Gayathri J		Dr. Rakhi S
CN lab-C2	Prof. Annappa C	VLSI lab c1	Prof. Spourthi S P
	Prof. Gayathri J		Dr. Rakhi S

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[Signature]
PROF. SP

PROF. & HEAD OF THE DEPARTMENT
DEPARTMENT OF ELECTRONICS & COMMUNICATION
ATRIA INSTITUTE OF TECHNOLOGY
ANANDNAGAR, BENGALURU - 560 024

[Signature]
PRINCIPAL 15/9/2023

Principal
Atria Institute of Technology
Anandanagar, Bengaluru-24